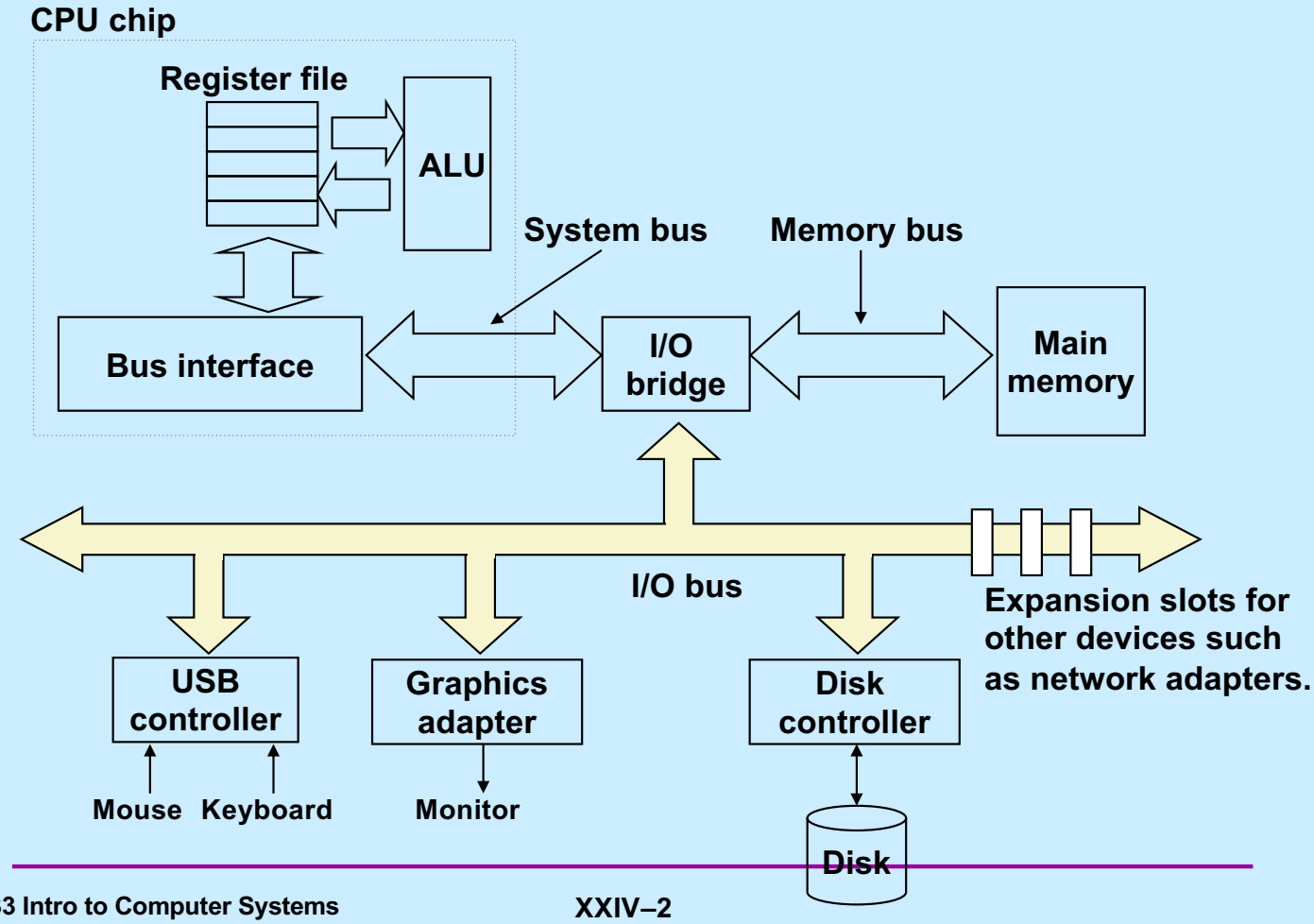
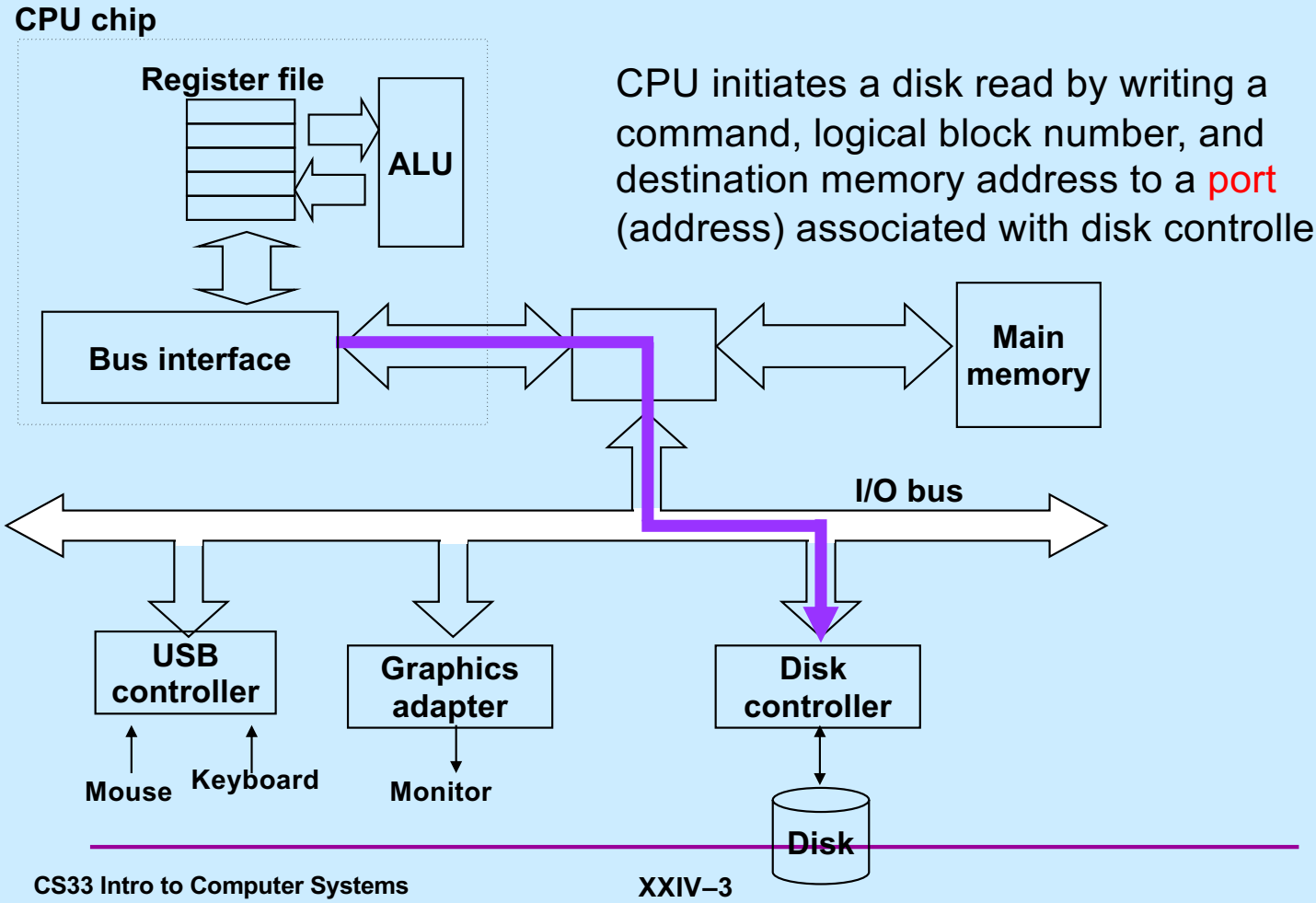
CS 33

Memory Hierarchy III

I/O Bus

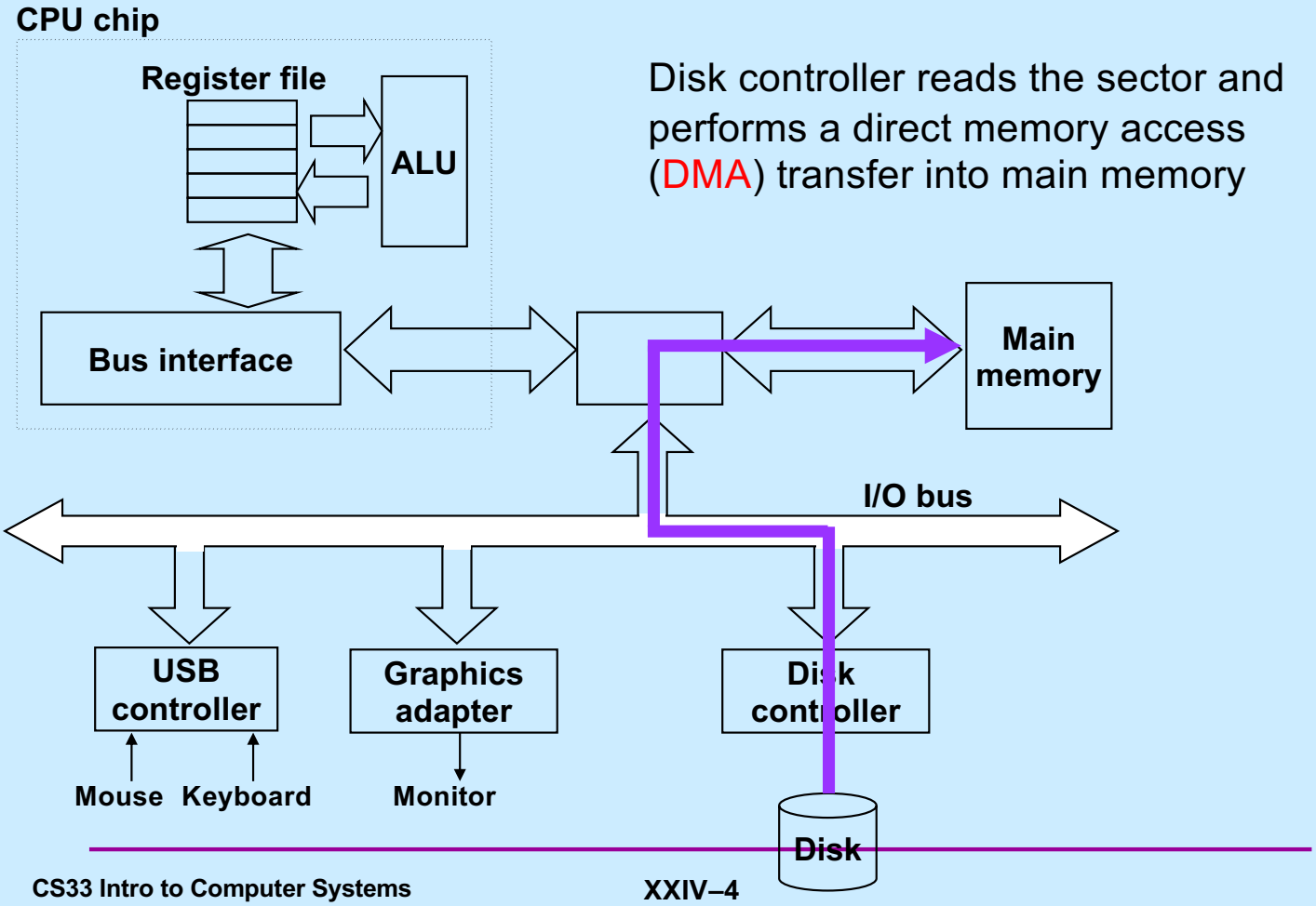

CPU chip
Register file
ALU
System bus
Memory bus
Bus interface
I/O bridge
Main memory
I/O bus
Expansion slots for other devices such as network adapters.
USB controller
Graphics adapter
Disk controller
Mouse
Keyboard
Monitor
Disk

Reading a Disk Sector (1)



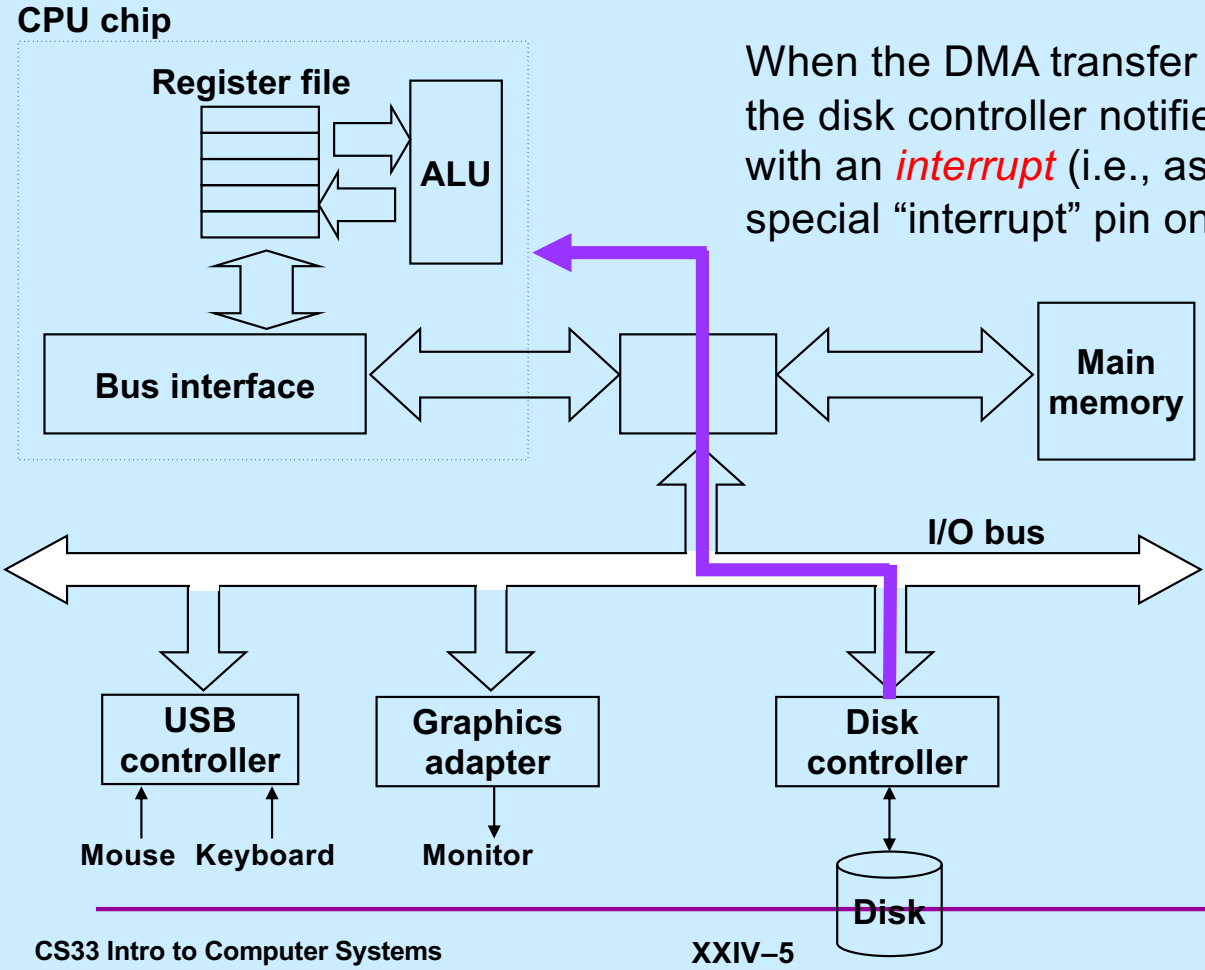
CPU initiates a disk read by writing a command, logical block number, and destination memory address to a port (address) associated with disk controller

Reading a Disk Sector (2)



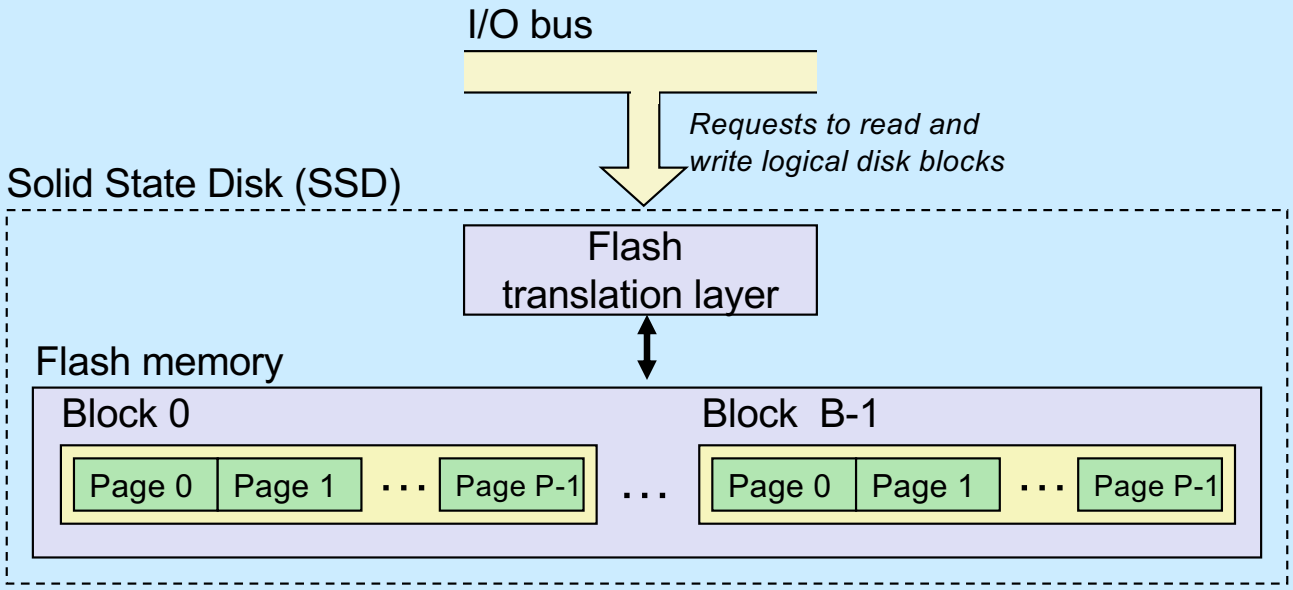
Disk controller reads the sector and performs a direct memory access (DMA) transfer into main memory

Reading a Disk Sector (3)



When the DMA transfer completes, the disk controller notifies the CPU with an *interrupt* (i.e., asserts a special "interrupt" pin on the CPU)

Solid-State Disks (SSDs)



- **Pages: 512KB to 4KB; blocks: 32 to 128 pages**
- **Data read/written in units of pages**
- **Page can be written only after its block has been erased**
- **A block wears out after 100,000 repeated writes**

SSD Performance Characteristics

Sequential read tput	250 MB/s	Sequential write tput	170 MB/s
Random read tput	140 MB/s	Random write tput	14 MB/s
Random read access	30 us	Random write access	300 us

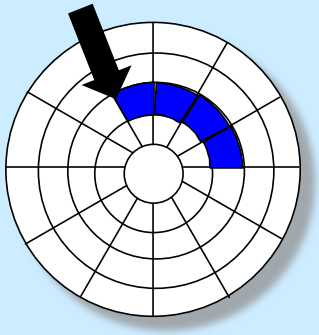
- Why are random writes so slow?
  - erasing a block is slow (around 1 ms)
  - modifying a page triggers a copy of all useful pages in the block
    - » find a used block (new block) and erase it
    - » write the page into the new block
    - » copy other pages from old block to the new block

SSD Tradeoffs vs Rotating Disks

- Advantages
  - no moving parts → faster, less power, more rugged
- Disadvantages
  - have the potential to wear out
    - » mitigated by "wear-leveling logic" in flash translation layer
    - » e.g. Intel X25 guarantees 1 petabyte (10^{15} bytes) of random writes before they wear out
  - in 2010, about 100 times more expensive per byte
  - in 2017, about 6 times more expensive per byte
  - in 2023, about 1-1.5 times more expensive per byte
- Applications
  - smart phones, laptops, desktops

Reading a File on a Rotating Disk

- Suppose the data of a file are stored on consecutive disk sectors on one track
  - this is the best possible scenario for reading data quickly
    - » single seek required
    - » single rotational delay
    - » all sectors read in a single scan



Quiz 1

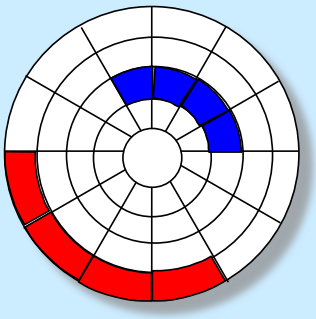
We have two files on the same (rotating) disk. The first file's data resides in consecutive sectors on one track, the second in consecutive sectors on another track. It takes a total of *t* seconds to read all of the first file then all of the second file.

Now suppose the files are read concurrently, perhaps a sector of the first, then a sector of the second, then the first, then the second, etc. Compared to reading them sequentially, this will take

a) **less time**

b) **about the same amount of time (within a factor of 2)**

c) **much more time**



Quiz 2

We have two files on the same solid-state disk. Each file's data resides in consecutive blocks. It takes a total of *t* seconds to read all of the first file then all of the second file.

Now suppose the files are read concurrently, perhaps a block of the first, then a block of the second, then the first, then the second, etc. Compared to reading them sequentially, this will take

a) **less time**

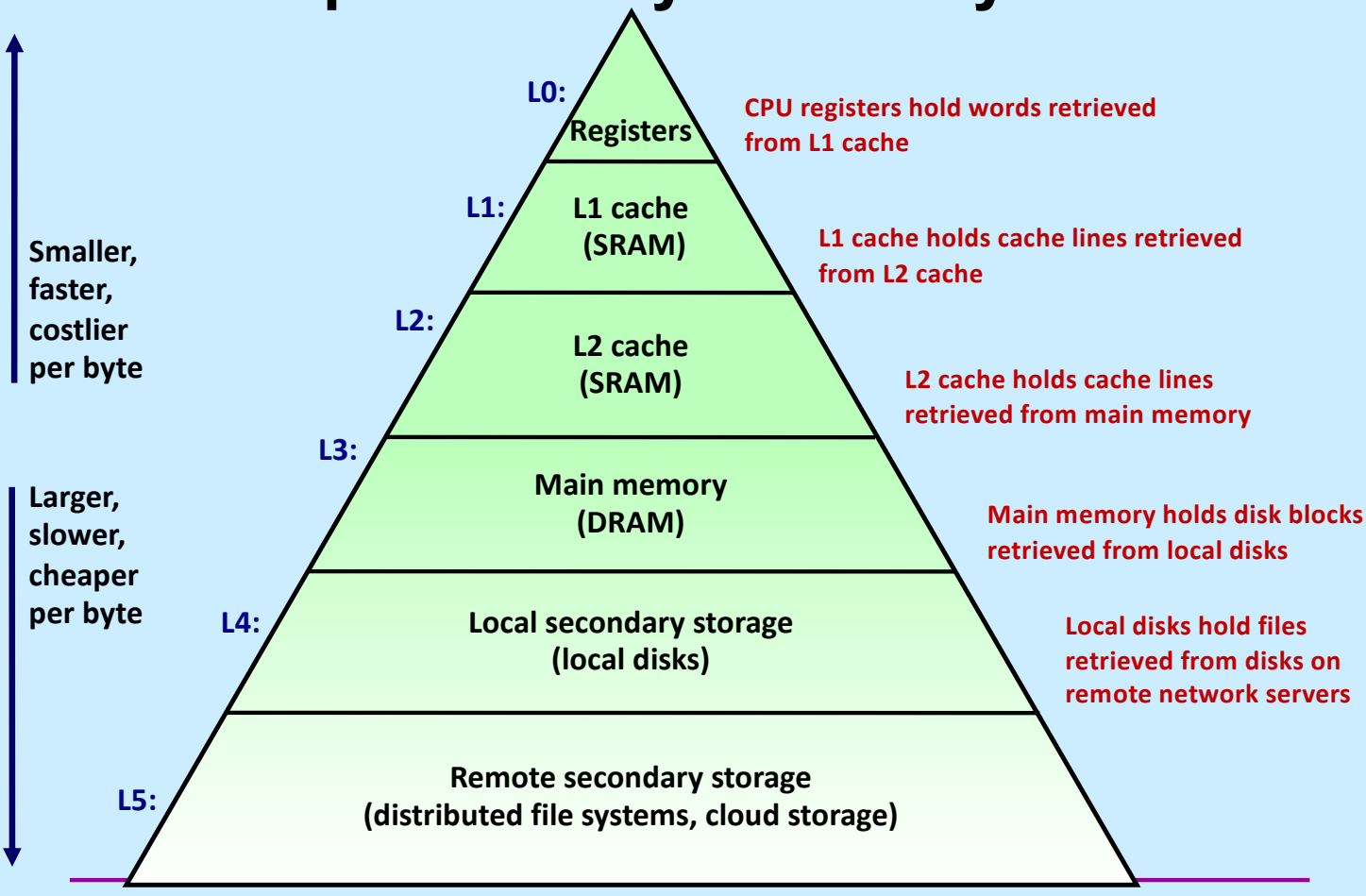
b) **about the same amount of time (within a factor of 2)**

c) **much more time**

Memory Hierarchies

- Some fundamental and enduring properties of hardware and software:
  - fast storage technologies cost more per byte, have less capacity, and require more power (heat!)
  - the gap between CPU and main memory speed is widening
  - well written programs tend to exhibit good locality
- These fundamental properties complement each other beautifully
- They suggest an approach for organizing memory and storage systems known as a memory hierarchy

An Example Memory Hierarchy



Putting Things Into Perspective ...

- Reading from:
  - ... the L1 cache is like grabbing a piece of paper from your desk (3 seconds)
  - ... the L2 cache is picking up a book from a nearby shelf (14 seconds)
  - ... main system memory (DRAM) is taking a 4-minute walk down the hall to talk to a friend
  - ... a hard drive is like leaving the building to roam the earth for one year and three months

Disks Are Still Important

- Cheap
  - cost/byte less than SSDs
- (fairly) Reliable
  - data written to a disk is likely to be there next year
- Sometimes fast
  - data in consecutive sectors on a track can be read quickly
- Sometimes slow
  - data in randomly scattered sectors takes a long time to read

Abstraction to the Rescue

- Programs don’t deal with sectors, tracks, and cylinders
- Programs deal with *files*
  - maze.c rather than an ordered collection of sectors
  - OS provides the implementation

Implementation Problems

- Speed
  - use the hierarchy
    - » copy files into RAM, copy back when done
  - optimize layout
    - » put sectors of a file in consecutive locations
  - use parallelism
    - » spread file over multiple disks
    - » read multiple sectors at once

Implementation Problems

- Reliability
  - computer crashes
    - » what you thought was safely written to the file never made it to the disk — it's still in RAM, which is lost
    - » worse yet, some parts made it back to disk, some didn't
      - you don't know which is which
      - on-disk data structures might be totally trashed
  - disk crashes
    - » you had backed it up … yesterday
  - you screw up
    - » you accidentally delete the entire directory containing your shell 1 implementation

Implementation Problems

- Reliability solutions
  - computer crashes
    - » transaction-oriented file systems
    - » on-disk data structures always in well defined states
  - disk crashes
    - » files stored redundantly on multiple disks
  - you screw up
    - » file system automatically keeps "snapshots" of previous versions of files

CS 33

Linkers

gcc Steps

1) Compile
   - to start here, supply .c file
   - to stop here: `gcc -S` (produces .s file)
   - if not stopping here, gcc compiles directly into a .o file, bypassing the assembler
2) Assemble
   - to start here, supply .s file
   - to stop here: `gcc -c` (produces .o file)
3) Link
   - to start here, supply .o file

The Linker

- An executable program is one that is ready to be loaded into memory
- The linker (known as ld: /usr/bin/ld) creates such executables from:
  - object files produced by the compiler/assembler
  - collections of object files (known as libraries or archives)
  - and more we'll get to soon ...

Linker's Job

- Piece together components of program
  - arrange within address space
    - » code (and read-only data) goes into text region
    - » initialized data goes into data region
    - » uninitialized data goes into bss region
- Modify address references, as necessary

A Program

```
int nprimes = 100;
int *prime, *prime2;
int main() {
   int i, j, current = 1;
   prime = (int *)malloc(nprimes*sizeof(*prime));
   prime2 = (int *)malloc(nprimes*sizeof(*prime2));
   prime[0] = 2; prime2[0] = 2*2;
   for (i=1; i<nprimes; i++) {
   NewCandidate:
      current += 2;
      for (j=0; prime2[j] <= current; j++) {
         if (current % prime[j] == 0)
            goto NewCandidate;
      }
      prime[i] = current; prime2[i] = current*current;
   }
   return 0;
}
```

data

bss

dynamic

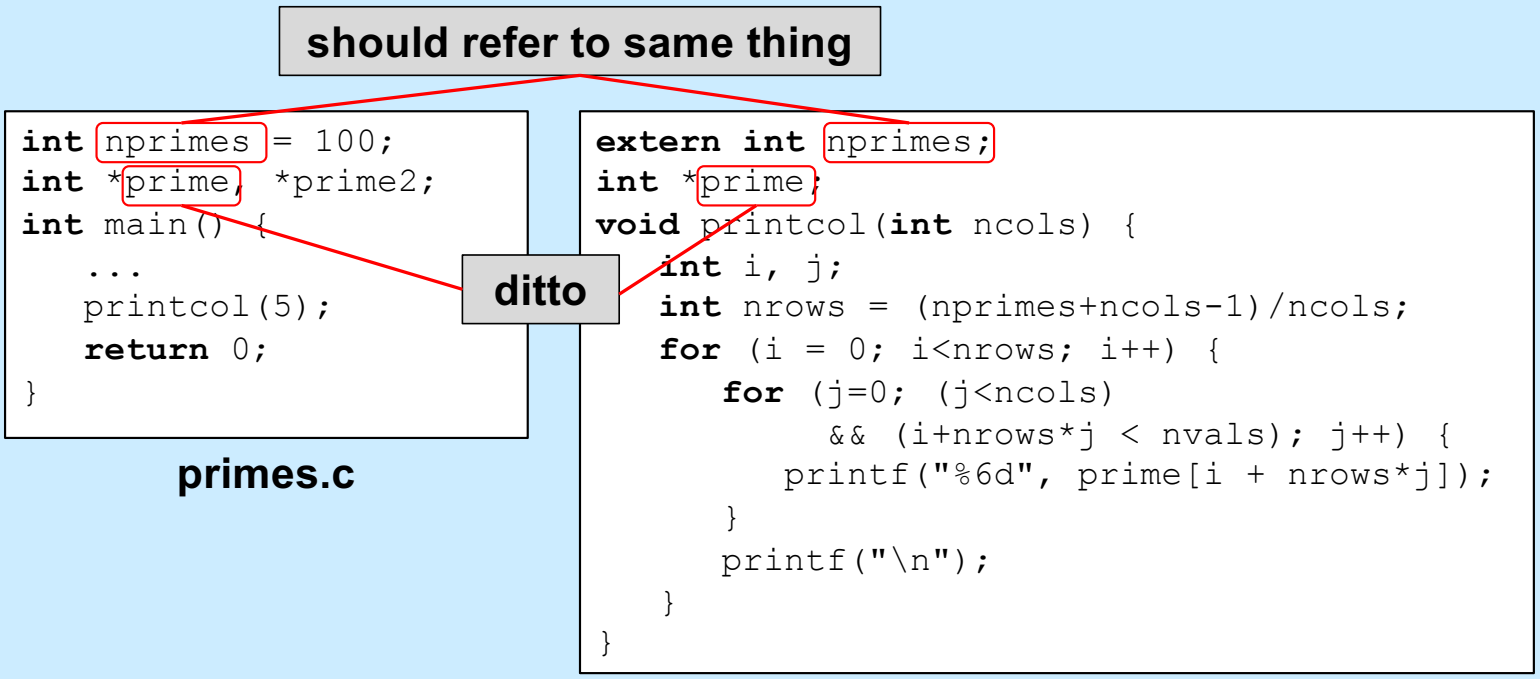
text

... with Output

```
int nprimes = 100;
int *prime, *prime2;
int main() {
   ...
   printcol(5);
   return 0;
}

void printcol(int ncols) {
   int i, j;
   int nrows = (nprimes+ncols-1)/ncols;
   for (i = 0; i<nrows; i++) {
      for (j=0; (j<ncols) && (i+nrows*j < nvals); j++) {
         printf("%6d", prime[i + nrows*j]);
      }
      printf("\n");
   }
}
```


... Compiled Separately



primes.c

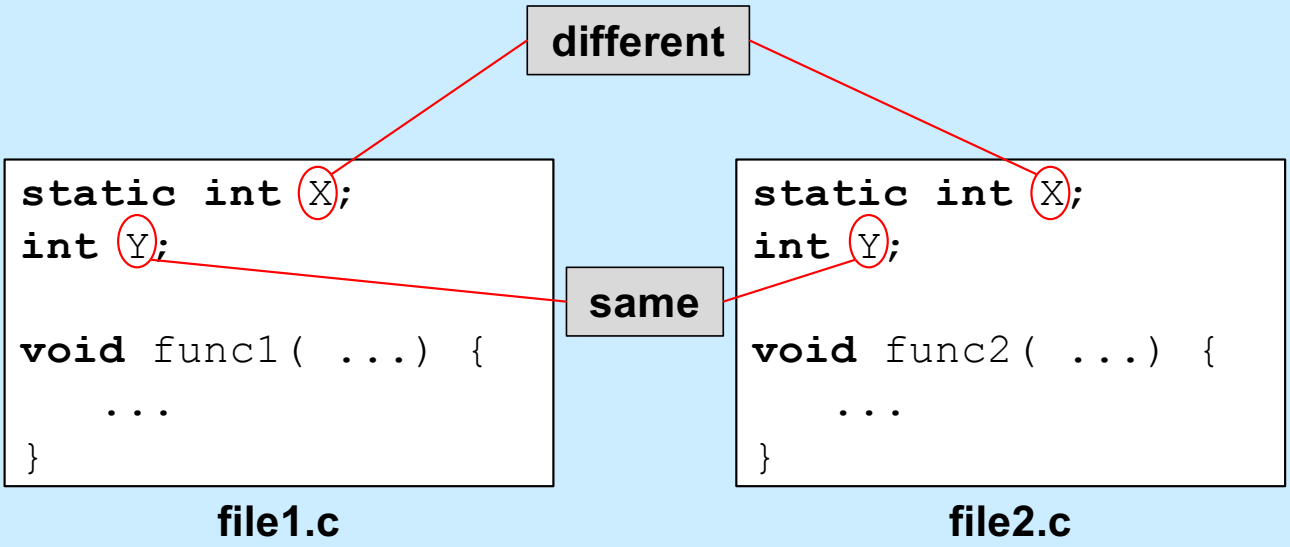
printcol.c

```
gcc –c primes.c
gcc –c printcol.c
gcc –o primes primes.o printcol.o
```

Global Variables

- Initialized vs. uninitialized
  - initialized allocated in *data* section
  - uninitialized allocated in *bss* section
    - » implicitly initialized to zero
- File scope vs. program scope
  - *static* global variables known only within file that declares them
    - » two of same name in different files are different
    - » e.g., `static int X;`
  - non-static global variables potentially shared across all files
    - » two of same name in different files are same
    - » e.g., `int X;`

Scope

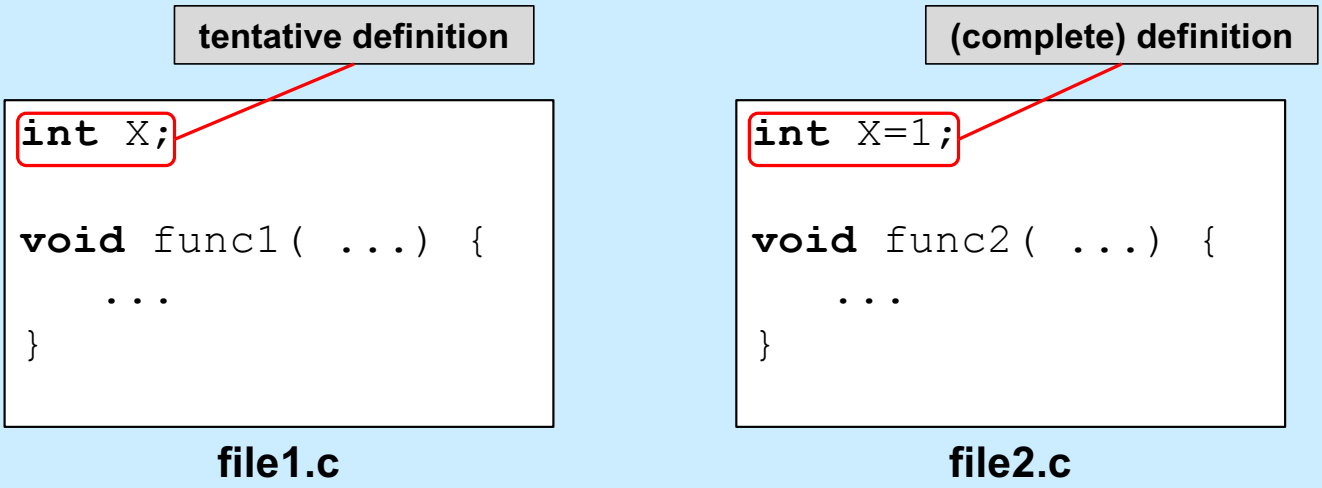


Static Local Variables

```
int *sub1() {
  int var = 1;
  …
  return &var;
  /* amazingly illegal */
}
```

```
int *sub2() {
  static int var = 1;
  …
  return &var;
  /* (amazingly) legal */
}
```

Reconciling Program Scope (1)



Where does X go?
What's its initial value?

- **tentative definitions overridden by compatible (complete) definitions**
- **if not overridden, then initial value is zero**

Reconciling Program Scope (2)

```
int X=2;

void func1( ...) {
    ...
}
```

file1.c

```
int X=1;

void func2( ...) {
    ...
}
```

file2.c

What happens here?

Reconciling Program Scope (3)

```
int X=1;

void func1( ...) {
    ...
}
```

file1.c

```
int X=1;

void func2( ...) {
    ...
}
```

file2.c

Is this ok?

Reconciling Program Scope (4)

```
extern int X;

void func1( ...) {
    ...
}
```

file1.c

```
int X=1;

void func2( ...) {
    ...
}
```

file2.c

What's the purpose of "extern"?

Does Location Matter?

```
int main(int argc, char *[]) {
   return(argc);
}
```

```
main:
  pushq %rbp    ; push frame pointer
  movq  %rsp, %rbp     ; set frame pointer to point to new frame
  movl  %edi, %eax     ; put argc into return register (eax)
  movq  %rbp, %rsp     ; restore stack pointer
  popq  %rbp    ; pop stack into frame pointer
  ret           ; return: pops end of stack into rip
```


Location Matters …

```
int X=6;
int *aX = &X;

int main() {
    void subr(int);
    int y=*aX;
    subr(y);
    return(0);
}

void subr(int i) {
    printf("i = %d\n", i);
}
```


Coping

- **Relocation**
  - **modify internal references according to where module is loaded in memory**
  - **modules needing relocation are said to be *relocatable***
    - » **which means they *require* relocation**
  - **the compiler/assembler provides instructions to the linker on how to do this**

A Revised Version of Our Program

```
extern int X;
int *aX = &X;
int Y = 1;

int main() {
    void subr(int);
    int y = *aX+Y;
    subr(y);
    return(0);
}
```

main.c

```
#include <stdio.h>
int X;

void subr(int XX) {
    printf("XX = %d\n", XX);
    printf("X = %d\n", X);
}
```

subr.c

```
gcc -o prog -O1 main.c subr.c
```

main.s (1)

```
            .file   "main.c"
0:          .text
0:          .globl  main
0:          .type   main, @function
0: main:
0: .LFB0:
0:          .cfi_startproc
0:          subq    $8, %rsp
4:          .cfi_def_cfa_offset 16
4:          movq    aX(%rip), %rax
11:         movl    (%rax), %edi
13:         addl    Y(%rip), %edi
19:         call    subr
24:         movl    $0, %eax
29:         addq    $8, %rsp
33:         .cfi_def_cfa_offset 8
33:         ret
34:         .cfi_endproc
34:.LFE0:
34:         .size   main, .-main
```

must be replaced with aX's address, expressed as an offset from the next instruction

must be replaced with Y's address, expressed as an offset from the next instruction

must be replaced with subr's address, expressed as an offset from the next instruction

main.s (2)

```
            .globl  Y
0:          .data
0:          .align 4
0:          .type   Y, @object
0:          .size   Y, 4
0: Y:
0:          .long   1
4:          .globl  aX
8:          .align 8
8:          .type   aX, @object
8:          .size   aX, 8
8: aX:
8:          .quad   X
8:          .ident  "GCC: (Debian 4.7.2-5) 4.7.2"
0:          .section        .note.GNU-stack,"",@progbits
```

Y should be made known to others

aX should be made known to others

must be replaced with address of X

subr.s (1)

```
            .file   "subr.c"
0:          .section        .rodata.str1.1,"aMS",@progbits,1
0:  .LC0:
0:          .string "XX = %d\n"
9:  .LC1:
9:          .string "X = %d\n"
```


subr.s (2)

```
0:          .text
0:          .globl  subr
0:          .type   subr, @function
0: subr:
0: .LFB11:
0:          .cfi_startproc
0:          subq    $8, %rsp
4:          .cfi_def_cfa_offset 16
4:          movl    %edi, %esi
6:          movl    $.LC0, %edi
11:         movl    $0, %eax
16:         call    printf
21:         movl    X(%rip), %esi
27:         movl    $.LC1, %edi
32:         movl    $0, %eax
37:         call    printf
42:         addq    $8, %rsp
46:         .cfi_def_cfa_offset 8
46:         ret
47:         .cfi_endproc
47:.LFE11:
47:         .size   subr, .-subr
```

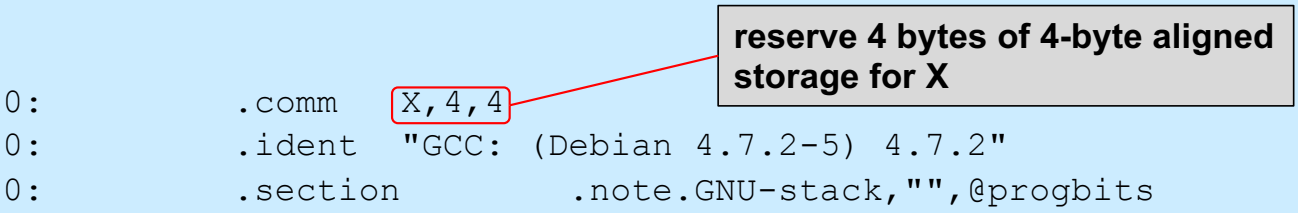
subr should be made known to others

must be replaced with .LC0's address

must be replaced with .LC1's address

must be replaced with printf's address, expressed as an offset from the next instruction

subr.s (3)




```
0:          .comm   X,4,4
0:          .ident  "GCC: (Debian 4.7.2-5) 4.7.2"
0:          .section        .note.GNU-stack,"",@progbits
```


Quiz 3

```
int X;
int func(int arg) {
  static int Y;
  int Z;

  ...

}
```

Which of *X*, *Y*, *Z*, and *arg* would the compiler know the addresses of at compile time?

a) all
b) just *X* and *Y*
c) just *arg* and *Z*
d) none